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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Filing Date	8/7/2003	
				First Named Inventor	Deng	
				Art Unit	Unassigned	
				Examiner Name	Unassigned	
Sheet	1	of	1	Attorney Docket Number	TI-35695	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JZ		BANBA ET AL., A CMOS Bandgap Reference Circuit with Sub-1-V Operation, IEEE Journal of Solid State Circuits,5/1999, 670-674, Vol. 34. No. 5	
			
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Examiner	166000		Date	
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